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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/769,330	01/30/2004	Philip B. James-Roxby	X-1557-2 US	7401
24309 7590 08/24/2009				
XILINX, INC				
ATTN: LEGAL DEPARTMENT				
2100 LOGIC DR				
SAN JOSE, CA 95124				
EXAMINER				
SEYE, ABDOUK				
ART UNIT		PAPER NUMBER		
2194				
MAIL DATE		DELIVERY MODE		
08/24/2009		PAPER		

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

**Advisory Action**  
**Before the Filing of an Appeal Brief**

**Application No.**

10/769,330

**Applicant(s)**

JAMES-ROXBYS ET AL.

**Examiner**

Abdou Karim Seye

**Art Unit**

2194

***--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --***

THE REPLY FILED 10 August 2009 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1. ☒ The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

- a) ☐ The period for reply expires \_\_\_\_\_ months from the mailing date of the final rejection.  
b) ☒ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.  
Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**NOTICE OF APPEAL**

2. ☐ The Notice of Appeal was filed on \_\_\_\_\_. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

**AMENDMENTS**

3. ☐ The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because:  
a) ☐ They raise new issues that would require further consideration and/or search (see NOTE below);  
b) ☐ They raise the issue of new matter (see NOTE below);  
c) ☐ They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or  
d) ☐ They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: \_\_\_\_\_. (See 37 CFR 1.116 and 41.33(a)).

4. ☐ The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).  
5. ☐ Applicant's reply has overcome the following rejection(s): \_\_\_\_\_.  
6. ☐ Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).  
7. ☒ For purposes of appeal, the proposed amendment(s): a) ☐ will not be entered, or b) ☒ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.  
The status of the claim(s) is (or will be) as follows:  
Claim(s) allowed: \_\_\_\_\_.  
Claim(s) objected to: \_\_\_\_\_.  
Claim(s) rejected: 1-20.  
Claim(s) withdrawn from consideration: \_\_\_\_\_.

**AFFIDAVIT OR OTHER EVIDENCE**

8. ☐ The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).  
9. ☐ The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing of good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).  
10. ☐ The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

**REQUEST FOR RECONSIDERATION/OTHER**

11. ☒ The request for reconsideration has been considered but does NOT place the application in condition for allowance because:  
See Continuation Sheet.  
12. ☐ Note the attached Information Disclosure Statement(s). (PTO/SB/08) Paper No(s). \_\_\_\_\_  
13. ☐ Other: \_\_\_\_\_.

/Hyung S. Sough/  
Supervisory Patent Examiner, Art Unit 2194  
08/21/09

Continuation of 11, does NOT place the application in condition for allowance because:

1. In the remarks applicant argue in substance that :
  - (1) He cited reference fail to teach or suggest " thread circuit", as recited in applicants' claims.
  - (2) He's "generic interconnections do not teach a specific interconnection topology among a plurality of thread circuits".
2. Examiner respectfully disagreed with applicant's remark.

As to point (1) He teaches that an IC design including parts (P1, p2,...) and wherein each part (P) is associated with thread (paragraph 24), and the multi-threaded grids/ IC design of FIG. 4 including partitions/parts (P1, p2,...) (paragraph 39-43). Therefore, the combination of these teaching of He's reference teach/ suggest " thread circuit" as part of the grids/IC design. Examiner has cited particular columns and line numbers and/or figures in the references as applied to the claims for the convenience of the applicant. Applicant is reminded that rejections are based on references as a whole and not just the cited passages. Although the specified citations are representative of the teachings in the art and are applied to the specific limitations within the individual claim, other passages and figures may apply as well. It is respectfully requested from the applicant, in preparing the responses, to fully consider the references in entirety as potentially teaching all or part of the claimed invention, as well as the context of the passage as taught by the cited art or disclosed by the examiner.

As to point (2) In response to applicant's argument that He's "generic interconnections do not teach a specific interconnection topology among a plurality of thread circuits". Examiner respectfully disagreed with applicant's remark, because, the teaching of interconnections of objects/cells/parts(Ps) in the IC design in (paragraph 13; FIG. 4) ; and the cells/connections in the IC design in paragraph (36) , and the generation of topology graph based of areas of the grids/ IC design that include parts (Ps) /threads (abstract; paragraph 23; and FIG. 4). The combination of these teaching of He's reference met the claimed limitation "interconnection topology among a plurality of thread ". Examiner has cited particular columns and line numbers and/or figures in the references as applied to the claims for the convenience of the applicant. Applicant is reminded that rejections are based on references as a whole and not just the cited passages. Although the specified citations are representative of the teachings in the art and are applied to the specific limitations within the individual claim, other passages and figures may apply as well. It is respectfully requested from the applicant, in preparing the responses, to fully consider the references in entirety as potentially teaching all or part of the claimed invention, as well as the context of the passage as taught by the cited art or disclosed by the examiner.